

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Mansoori et al. Art Unit: 2812  
Serial No.: 10/620,492 Examiner: Lindsay, Jr., W.  
Filing Date: 07/16/2003 Docket No.: TI-35375  
Customer No.: 23494 Conf. No.: 9069  
Title: METHOD TO REDUCE TRANSISTOR GATE TO SOURCE/DRAIN OVERLAP  
CAPACITANCE BY INCORPORATION OF CARBON

**AMENDMENT UNDER 37 CFR 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATION OF TRANSMISSION**

I hereby certify that the following papers are being transmitted electronically to the U.S. Patent and Trademark Office on 9/13/2006.

/Jacqueline J Garner/  
Jacqueline J. Garner  
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated April 4, 2006. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

**Amendments to the Claims** are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 6.